

FIG. 1A

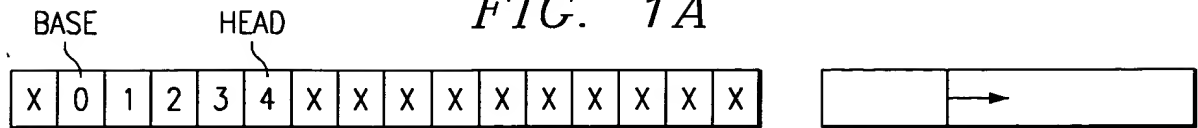


FIG. 1B

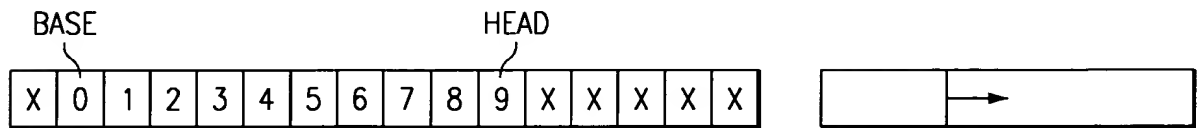


FIG. 2A

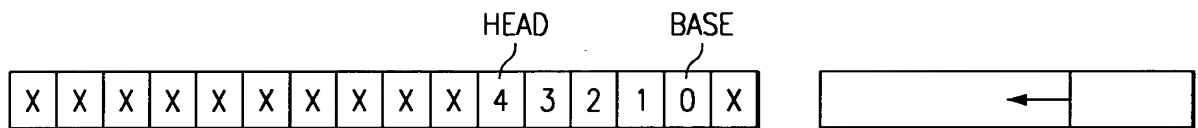
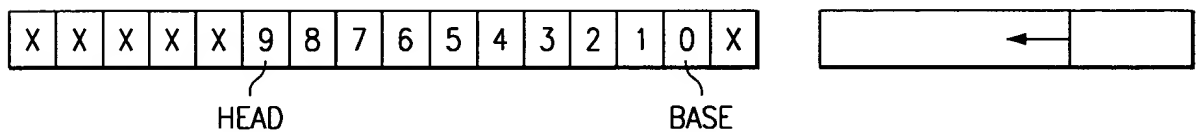
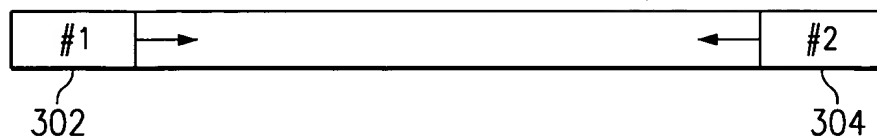


FIG. 2B



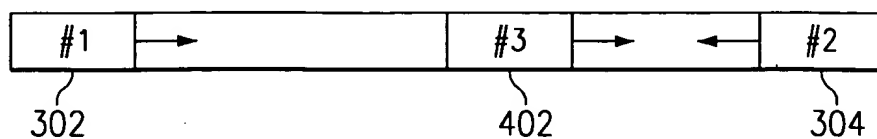
MEMORY ARRAY
300

FIG. 3



MEMORY ARRAY
300

FIG. 4



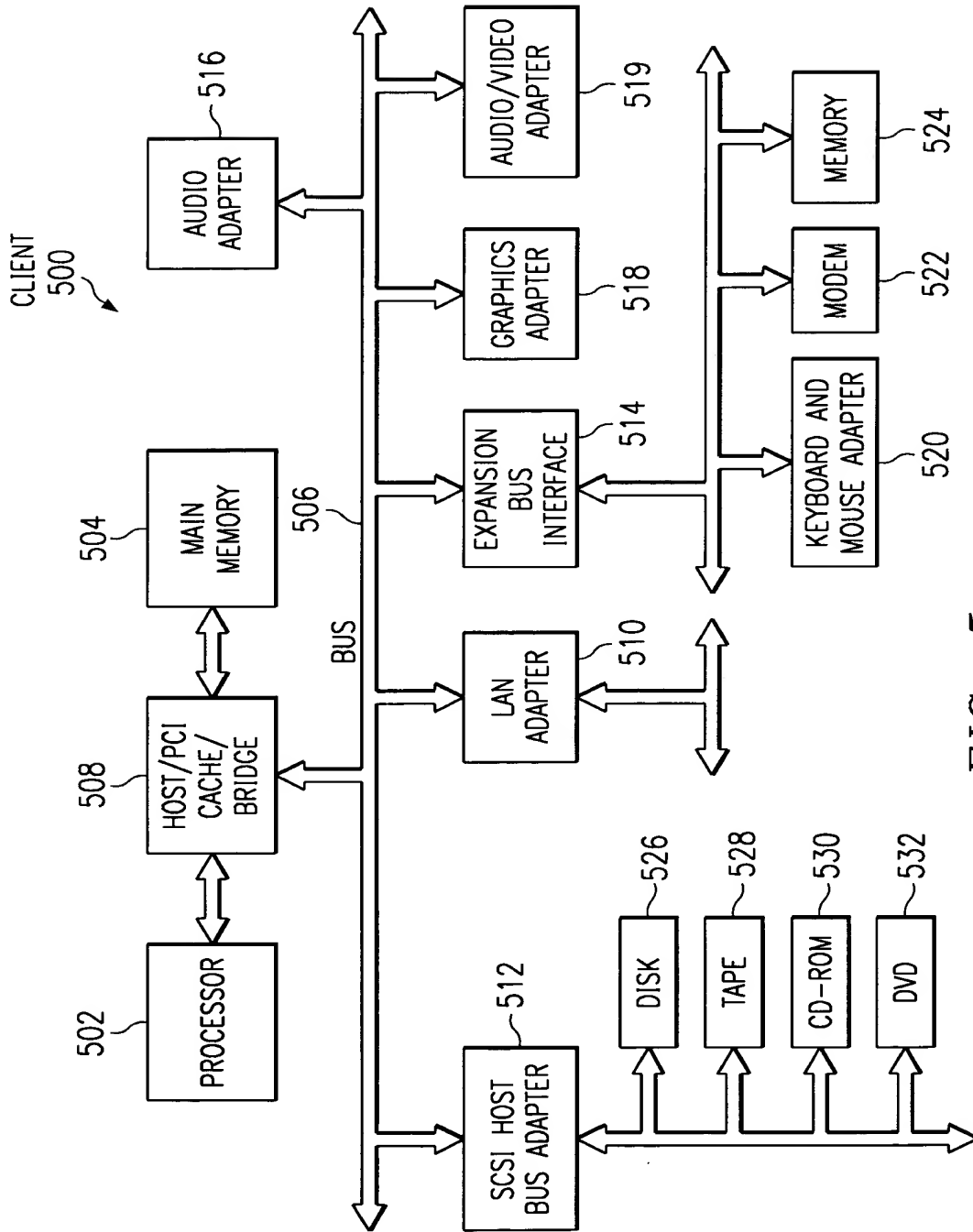
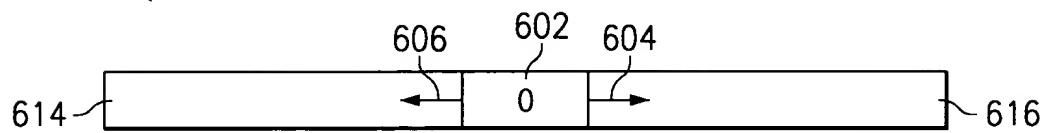


FIG. 5

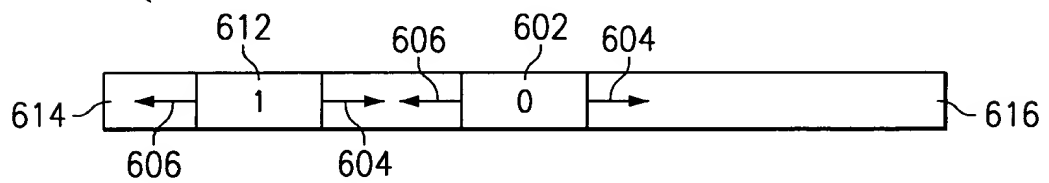
ONE BI-DIRECTIONAL
STACK
600

FIG. 6A



TWO BI-DIRECTIONAL
STACKS
600

FIG. 6B



THREE BI-DIRECTIONAL
STACKS
600

FIG. 6C

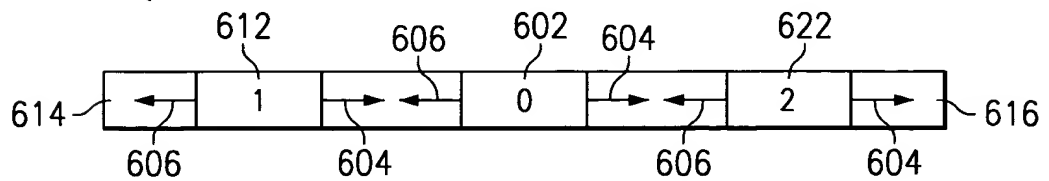


FIG. 7A
(PRIOR ART)

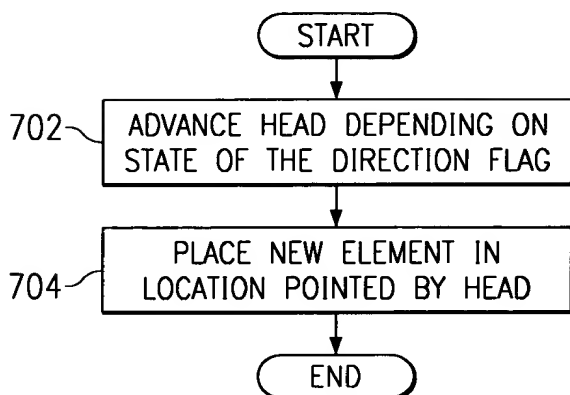


FIG. 7B

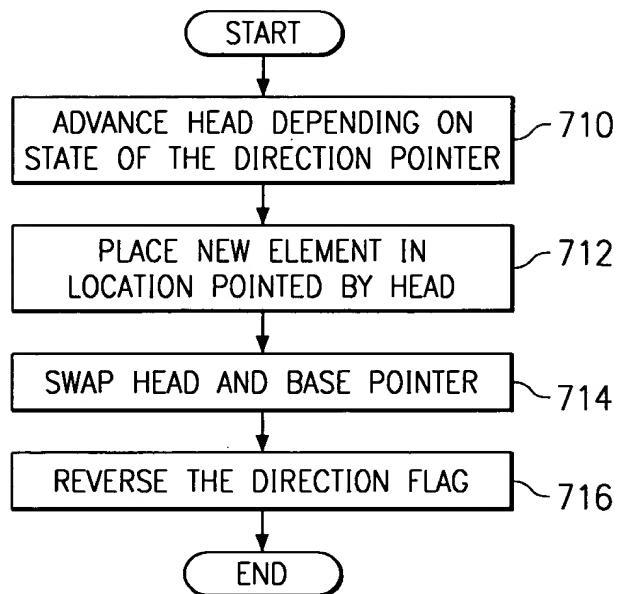


FIG. 9A
(PRIOR ART)

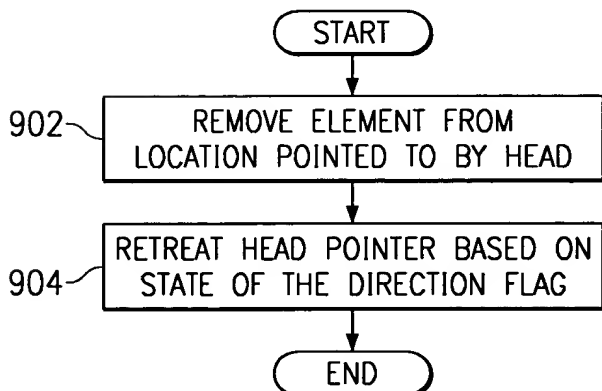


FIG. 9B

